	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	401	(integrated with circuit) and (substrate with via) and (contact with via) and notch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/16 12:10
2	BRS	L2	234	1 and (adhesive or adhesion)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/16 12:10
3	BRS	L3	17	2 and "439"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/16 12:13
4	BRS	L4	217	2 not 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/16 12:41
5	BRS	L5	0	10/716,299	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/16 12:41

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